

ND-363US  
Amendment dated 08/19/2004

09/708,516  
Reply to office action mailed 05/19/2004

01910030aa

**Amendments to the Specification:**

Please replace the title on page 1, lines 1 and 2, with the following rewritten title:

PORTABLE TELEPHONE SET FOR DETECTING RADIO WAVE WITH  
INTERFERENCE ON SWITCHED PERCH CHANNELS AND  
DISPLAYING INTERFERENCE WARNING FUNCTION DISPLAYED  
~~WITH INDICATION OF A RATE OF OCCURRENCE OF~~  
~~RETRANSMISSION~~

Please replace the paragraph beginning at page 14, line 18, with the following rewritten paragraph:

Then, the signal processing section 13 notifies the control circuit section 15 of analysis information of the layer 1 and broadcast information. This information is from within the received signal. The control circuit section 15 analyzes the layer 1 information and broadcast information (step S37). The analysis information in the notification includes frame synchronization, a bit error rate, a color code and so forth. The broadcast information includes a waiting permission level, control channel (CCH) structure information, restriction information and so forth. If the level information received from the radio circuit section 12 and the information received from the signal processing section 13 do not satisfy the waiting condition (NO in the step S38), then the control circuit section 15 ~~discriminates the~~ determines whether there is a radio wave interference condition (step S39). If the ~~discrimination~~ determination in step S39 is "YES" because the waiting condition is not satisfied ~~since the~~ as a result of the presence of a radio wave interference condition ~~is present~~, then the control circuit section 15 controls the warning display section 18 to display a warning of "Radio wave interference is present" by some suitable means (step S40)

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and investigates those remaining peripheral perch channels which satisfy the switching condition in order to select a next peripheral perch channel having a level higher than the predetermined level. This selection is made from within the channel table of the storage circuit section 16 (step S41), whereafter the control circuit section 15 returns its processing to step S34.